

Docket No.: 2207/8754

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

jc598 U.S. PTO  
09/536452  
03/28/00



APPLICANT : RONEN et al  
SERIAL NO. : Not assigned  
FILING DATE : March 28, 2000  
GROUP ART UNIT : Not assigned  
FOR : CONFINING AN APPLICATION TO A 32-BIT ADDRESS SPACE SUBSET IN MA 64-BIT PROCESSOR  
EXAMINER : Not assigned

Assistant Commissioner of Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97**

SIR:

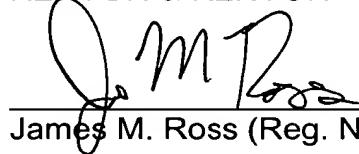
In accordance with Applicant's duty of disclosure under 37 C.F.R. §1.56(a) and §1.97(b), the references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention.

In compliance with 37 C.F.R. §1.98, copies of the references are submitted herewith.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,

KENYON & KENYON

  
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Date: March 28, 2000

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